

THE STATE SPACE MYSTERY IN MULTIPLE-VALUED LOGIC CIRCUIT WITH LOAD PLANE – PART I

*Viktor ŠPÁNY, *Pavol GALAJDA, **Milan GUZAN

*Department of Electronics and Multimedial Communications, Faculty of Electrical Engineering and Informatics, Technical University of Košice, Park Komenského 13, 041 20 Košice, tel. 055/602 4169, E-mail: Pavol.Galajda@tuke.sk

**Department of Theoretical Electrotechnics and Electrical Measurement, Faculty of Electrical Engineering and Informatics, Technical University of Košice, Park Komenského 3, 043 89 Košice, tel. 055/602 2706, E-mail: guzan@tuke.sk

SUMMARY

In the article presented we deal with new knowledge of the behaviour of dynamical systems both from theoretical and practical point of view.

Switching sequential circuits are an indispensable part of many modern electronic devices, such as memory cells, flip-flop sensors and many others. Since the invention of flip-flop switching circuits, the study of their dynamic behaviour has played an ever-increasing role. The dynamic properties of sequential circuits can be investigated by means of switching between the system's attractor. In this paper the boundary surfaces are discussed that play a crucial role in the process of switching.

By "analysis of the multivalued logic (MVL) circuit" we understand graphical representation of boundary surfaces that divide the basins of attraction. Each region of attraction contains one stable equilibrium, i.e. a stable singularity or a stable limit cycle. At existence of stable limit cycle are boundary surfaces very complicated and the control of such MVL circuit would most probably be problematic. Therefore we expected that when the stable limit cycles are absent the shape of the boundary surfaces will be simple and therethrough investigated structure would be more simply controlled. The simulation of the MVL structure shown that no always is morphology of the boundary surfaces simple when the stable limit cycles are absent. The knowledge about of morphology of the boundary surfaces corresponding to stable attractors makes it possible to design reliable methods of control of MVL structures.

Keywords: state space, boundary surface, singularities, piecewise-linear approximation, eigenvalue.

1. INTRODUCTION

The objective in this paper will be an analysis of a MVL consisting of two resonant tunnel diodes (RTD) connected in series. One of the RTDs is an element and the other represents a load. Since the load exhibits negative dynamic resistance, a natural question arose as to the behavior of this object described by equations (2) later in the text. Concept of the research about MVL circuit was first internally published in [10]. The first simulation results shown extreme distinguished from simulations refer to so-called "classic bistability" in [9], [19].

The very first simulations of the system's behavior suggested that the title STATE SPACE MYSTERY in [3] is not exaggerated at all. Since the RTDs are, according to [21], [1], [7], able to work at gigahertz frequencies, an analysis of these objects has considerable practical significance also. The advantages of MVL from the viewpoint of their transfer to higher orders are described in work [7].

By "analysis of the MVL circuit" we understand graphical representation of boundary surfaces (BS) that divide the basins of attraction. Each region of attraction contains one stable equilibrium, i.e. a stable singularity or a stable limit cycle. The algorithm for the calculation of the BS was first published in [12]. The significance of BS was also described in paper [6], which was included in the

book [2], since it deals with boundary surfaces in the context of Chua's circuit and a control pulse.

2. THE CIRCUIT OF THE MULTIVALUED MEMORY

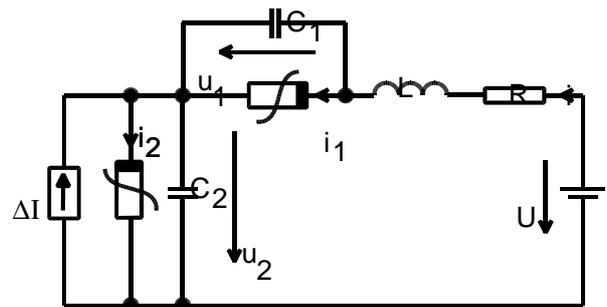


Fig.1 Model of the memory cell

The above-mentioned MVL circuit is shown in Fig.1 where the symbols of nonlinear elements correspond to resonant tunnel diodes. Let $i_2(u_2)$ and $i_1(u_1)$ represent the $V-I$ characteristics of the element and the „negative“ load respectively. Both characteristics are piecewise-linear (PWL) characteristics. General algebraic form of the PWL characteristics was first derived in [11]. Characteristics are defined by the expression:

corresponding to the plane $u_1 = 91mV$ which is the value corresponding to the saddle N_2 . Similarly, in the plane u_1, u_2 is the cross-section through the singularity N_2 ($i = 2, 9mA$). There is also the trace of the tangent plane (EG_2) to the double surfaces separating the attractors S_2, S_3 . This trace of the tangent plane is tangent to the cross-section of the boundaries in N_2 . This simulation test has been first provided in [17].

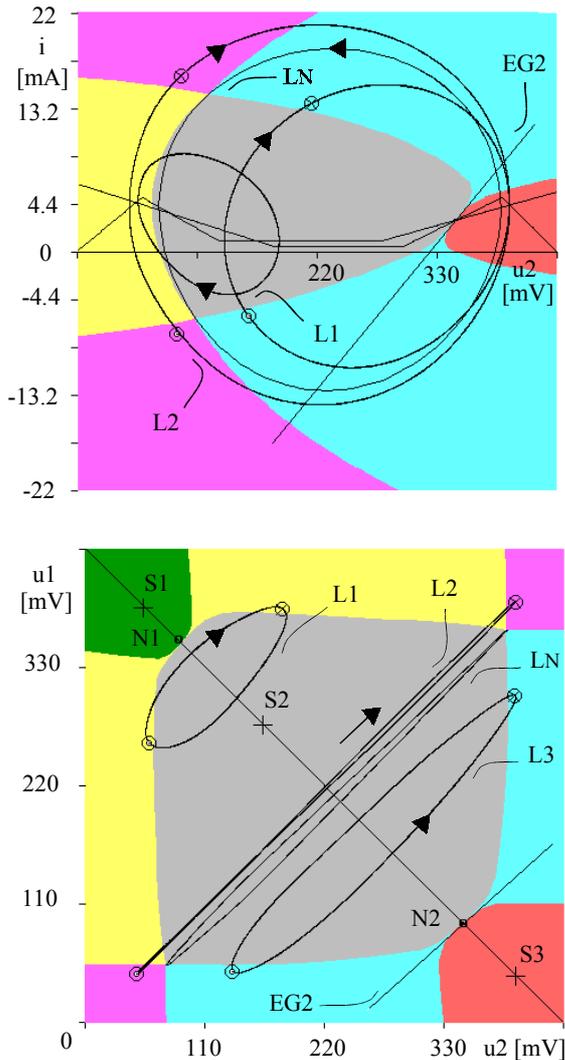


Fig. 3 The Monge's projection of the cross-section (in singularity N_2) of the boundary surfaces and stable limit cycles (L_1, L_2, L_3) and unstable limit cycle (L_N).

According to [12] this tangent plane is given by the equation:

$$y_1 = \alpha_{i1} \Delta u_1 + \alpha_{i2} \Delta u_2 + \alpha_{i3} \Delta i = 0 \quad (5)$$

where the eigenvectors α_{ij} , correspond to the dominant eigenvalue of the Jacobi matrix as was

first reported in [12]. The values of the eigenvectors for both saddle points are given in *Tab. 1*.

Eq uili br.	co-ordinates			eigenvector		
	u_{i1} [mV]	u_{i2} [mV]	i_i [mA]	α_{i1}	α_{i2}	α_{i3}
S1	385	54	4,5	*	*	*
N1	353	86	3,5	8,2922	-0,8703	1
S2	275	164	1	*	*	*
N2	91	348	2,9	-8,2028	-1,1059	1
S3	43	397	4,3	*	*	*

Eq uili br.	Eigenvalues $\times 10^9$		
	λ_{i1}	$\text{Re}\{\lambda_{i23}\}$	$\text{Im}\{\lambda_{i23}\}$
S1	-123.213593379	-53.693203310	185.870855376
N1	31.277885807	9.461057096	178.912952777
S2	-32.840127795	-15.579936102	196.809479969
N2	25.828651316	8.985674341	183.943148711
S3	-145.801276658	-55.199361671	179.155674391

Tab. 1 The numerical values of the co-ordinates, eigenvalues and eigenvector for equilibria corresponding to memory cell in *Fig.1*. The bias voltage of the memory cell $U=440mV$; $L=1e-10H$, $C_1=C_2=5e-13F$, $R=0\Omega$. The parameter values corresponding to active and load device are as follows: ${}^1g_0=0,1$; ${}^1g_1=-0,05$; ${}^1g_2=0$; ${}^1g_3=0,032$; ${}^2g_0=0,0833$; ${}^2g_1=-0,0571$; ${}^2g_2=0$; ${}^2g_3=0,0281[S]$; ${}^1U_1=50$; ${}^1U_2=140$; ${}^1U_3=260$; ${}^2U_1=60$; ${}^2U_2=130$; ${}^2U_3=280 [mV]$.

From the viewpoint of morphology the surfaces and the corresponding regions of attraction exhibit unusual properties since in addition to the three stable singularities S_1, S_2, S_3 , there are also three stable limit cycles L_1, L_2, L_3 which are not coupled with the nonstable limit cycles as was the case with positive load in the cases described in [6], [9], [13] and [8]. In this case we have only one unstable limit cycle, denoted by L_N in *Fig. 3*. It is located on the border of four regions of attraction corresponding to L_1, L_2, L_3, S_2 . On the surface of the bounded region corresponding to singularity S_2 is the limit cycle L_N which was detected in the computer simulation by integration in backward time. Hence L_N is not saddle-type as in the case of positive load in [6], [9], [13] and [8], which means there are no initial conditions whose trajectories are attracted towards L_N as was the case in [18] and [9].

From the practical viewpoint, however, unstable oscillatory phenomena are undesirable. In such a case this analysis is valuable in that it enables to verify the parameter values for which the oscillatory phenomenon is absent. It is most likely that this will occur when the absolute value of negative normed conductance of both RTDs will be less than 1, or the practical value $R>0$ will be assumed.

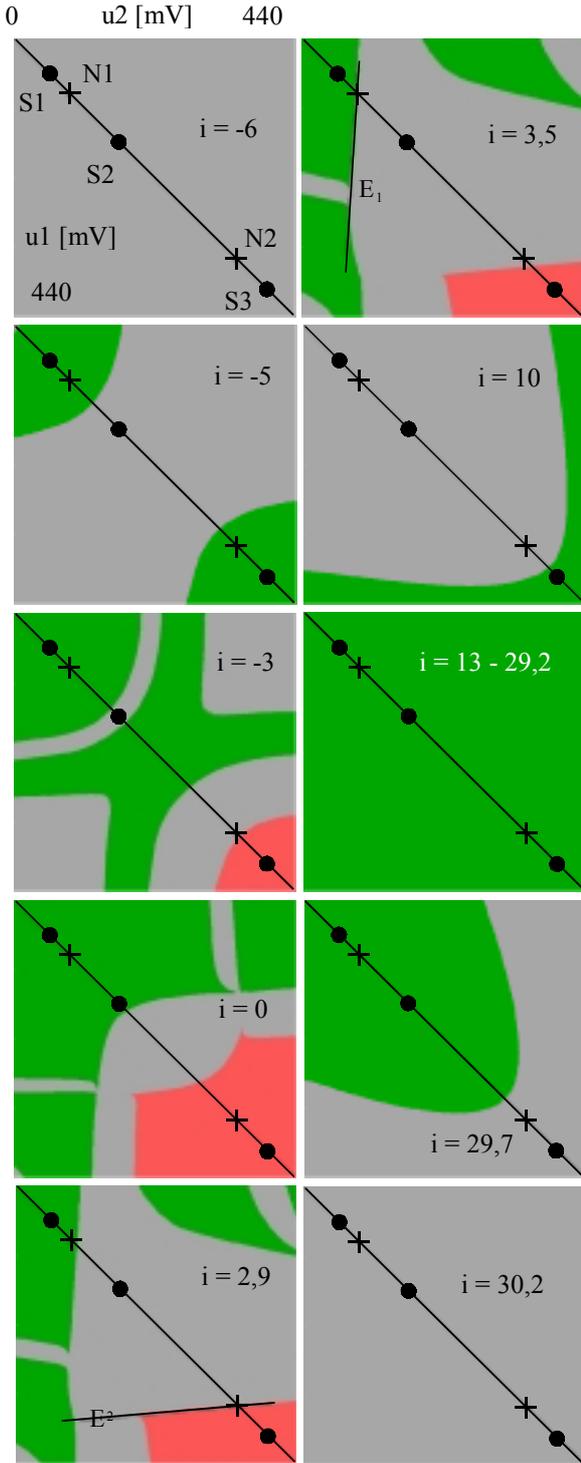


Fig. 4 The cross-sections of the attractors, for corresponding stable states at different current levels and projection onto u_1, u_2 -plane. The different gray-scale areas represent the domains of attraction for sinks S_1, S_2, S_3 . Depicted are the both traces of tangential planes E_1, E_2 as well. Capacitances were chosen $C_1 = C_2 = 3e - 14F$.

4. MORPHOLOGY OF THE BOUNDARY SURFACE WHEN LIMIT CYCLES ARE ABSENT

In *Fig. 4*, *Fig. 5* and *Fig. 6* we present cross sections of boundary surfaces, when the limit cycles are absent. The capacitances of the circuit in *Fig. 1* are the bifurcation parameters. *Fig. 4*, and *Fig. 5* are related to the values $C_1 = C_2 = 3e - 14F$ and $C_1 = C_2 = 4e - 14F$ respectively. The eigenvalues of the Jacobi matrix corresponding to the saddle point N_1 for $C_1 = C_2 = 3e - 14F$ are

$$\lambda_1 = 17.22358266519e+11$$

$$\lambda_2 = -6.27750395530e+11$$

$$\lambda_3 = -2.57941204322e+11$$

and similarly the eigenvalues for the saddle point N_2 are

$$\lambda_1 = 14.57963369881e+11$$

$$\lambda_2 = -3.63981684940e+11 - 1.85517698577e+11i$$

$$\lambda_3 = -3.63981684940e+11 + 1.85517698577e+11i$$

Interesting in this case is the shape of the region of attractions. The darkest gray area corresponds to region of attraction of the singularity S_1 , the lightest gray area corresponds to region of attraction of S_2 and gray area corresponds to region of attraction of S_3 . The cross section at current level $i = -6mA$ (in *Fig. 4* it match to note $i = -6$, the same convention is used for all subfigures) is the first one to show the region of attraction only for stable singularity S_2 . At the level $i = -5mA$ this region is branching, whereas from $i = -3mA$ to $i = 3,5mA$ there are already very complicated structure of boundary surfaces. At higher current levels there is the morphology of boundary surfaces simple again. From current $13 - 29.2mA$ there is the region of attraction only for stable singularity S_1 . The shape of the region of attractions can be clearer, as well, from cross-sections parametrized by u_1 in *Fig. 5* (notation of the cross sections at different voltage levels and different gray-scale areas of attraction used in this figure are the same as mentioned for *Fig. 4*). The sections, parametrized by u_1 , clearly document the morphological complexity of the corresponding regions. Therefore the control of such a ternary would most probably be problematic.

More simple shape of the region of attractions (useful for the control of such a ternary, for example) is depicted in *Fig. 6*. The capacitances of the circuit in *Fig. 1* correspond to the regions of attraction in *Fig. 6* was chosen $C_1 = C_2 = 9e - 14F$.

Notation of the cross sections at different current levels and different gray-scale areas of attraction used in this figure are the same as mentioned for Fig.4.

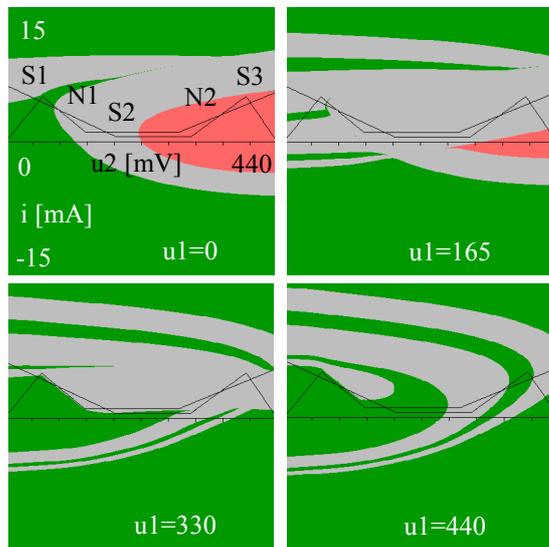


Fig. 5 The cross-section of the attractors, for corresponding stable states at different voltage levels (u_1) and projection onto i, u_2 -plane. The different gray-scale areas represent the domains of attraction for sinks S_1, S_2, S_3 . Capacitances were chosen $C_1 = C_2 = 4e - 14F$.

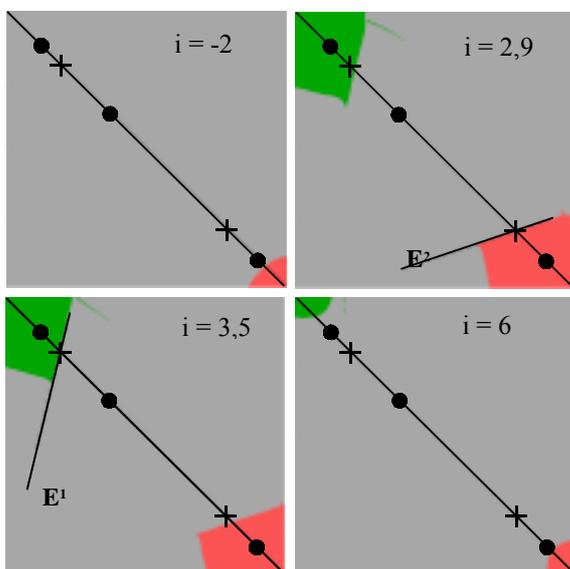


Fig. 6 The cross-section of the attractors, for corresponding stable states at different current levels and projection onto u_1, u_2 -plane. The different gray-scale areas represent the domains of attraction for sinks S_1, S_2, S_3 . Depicted are the both tangential planes E_1, E_2 as well. Capacitances were chosen $C_1 = C_2 = 9e - 14F$.

5. CONCLUSION

From the viewpoint of multiple-valued logic based on RTD diodes the most significant result is one corresponding to the nonzero R in Fig.1. Those values of R that were considered in this contribution could correspond to the resistance of the sources to which the circuit is connected. The fact how dramatically the number of singularities is affected by the value of R was first published in [15]. The method of analysis of MVL, demonstrated in this contribution gives the possibility of reliable design of MVL structures.

The morphology of the basins of attraction, corresponding to stable attractors makes it possible to design reliable methods of control of MVL structures, which was first published in [16], [14], and later in [18] and [4].

REFERENCES

- [1] Butler, J.T.: "Multiple-valued logic". Potentials IEEE, April/May 1995, pp 11-14.
- [2] Chua, Leon O.: "Chua's Circuit: A Paradigm for Chaos." World Scientific, pp. 249-281, 1997.
- [3] Galajda, P., Guzan, M., Špány, V.: The state space mystery with negative load. Radioengineering, Vol. 8, 1999, No. 2, pp. 2-7.
- [4] Guzan, M., Špány, V., Galajda, P.: The state space mystery in multiple valued logic circuits. The 4th international conference on DSP, September 1999, pp. 151-154, Herľany, Slovakia.
- [5] Horváth, L.: Multivalued logic constructed by means of one ports. Diploma thesis TU Košice. Košice, 1996, in Slovak.
- [6] Pivka, L., Špány, V.: "Boundary Surfaces and Basin Bifurcations in Chua's Circuit," Journal of Circuits, Systems and Computers, Vol. 3, No. 2, 1993, pp. 441-470.
- [7] Seabaugh, C. A., Kao, Y. Ch., Yuan, H. T.: "Nine State Resonant Tunneling Diode Memory." IEEE Electron Device Letters, vol. 13, no. 9, September 1992, pp. 479-481.
- [8] Stern, T.: Theory of nonlinear networks and systems. Adison Wesley publishing company, INC. 1965.
- [9] Špány, V.: "Special Surfaces and Trajectories of the Multidimensional State Space." Zborník VŠT v Košiciach 1, 1978, pp. 123-152, in Slovak.
- [10] Špány, V.: "Negative Load Resistance and the Basins of Attraction." Internal information on the Department of Radioelectronics, pp.1-5, August 1994.
- [11] Špány, V.: "The Expression of the Non-Linear Characteristics by Means of Absolutes Values", Slaboproudý obzor, Vol. 40, No. 7, 1988, pp. 354-356, in Slovak.

- [12] Špány, V.: “Graphical Solution of the Non-linear Circuit with the Help of the M-dimensional State Space.” *Elektrotechnický časopis*, no.4 , 1969, pp. 233-248, in Slovak.
- [13] Špány, V.: Multistable systems and special surfaces of the m-dimensional state space. *Elektrotechnický časopis*, Vol. 33, No. 7, pp. 551-556, in Slovak.
- [14] Špány, V.: Trigger pulse in a circuit with tunnel diode. *Slaboproudý obzor*, Vol. 26, 1965, pp. 246-247, in Slovak.
- [15] Špány, V.: The load plane and singularities in MVL circuit. Internal information for doctorands on Department of Electronics and Multimedia Telecommunications (DEMT), pp.1-3, August 1999.
- [16] Špány, V.: The analysis of a one-tunnel diode binary. *Proceedings IEEE*, Vol. 55, 1967, pp. 1089-1090.
- [17] Špány, V.: The functioning of the boundary surface element in the case of the all positive real parts of eigenvalues – graphical testing. Internal information for doctorands on DEMT, pp.1-5, April 1999.
- [18] Špány, V., Pivka, L.: “Boundary Surfaces in Sequential Circuits,” *International Journal of Circuit Theory and Applications*, vol. 18, 1990, pp. 349-360.
- [19] Špány, V., Pivka, L.: “Boundary Surfaces in sequential circuits.” *Beitrage zur Theoretischeu Elektrotechnik, Das Internationale Symposium in Ilmenau*, 1988, pp.90-99.
- [20] Špány, V., Pivka, L.: Invariant manifolds and generation of chaos. *Elektrotechnický časopis*, Vol. 39, 1988, pp.417-431.
- [21] Wei, S. J., Lin, H. Ch.: “A Multi-State Memory Using Resonant Tunneling Diode Pair,” 1991 *IEEE International Symposium on Circuits and Systems*, Singapore, June 1991, pp. 11-14.

BIOGRAPHY

Viktor Špány (Prof., Ing., DrSc.), received his DrSc (PhD) degree from the Slovak University of Technology in Bratislava, Czechoslovakia. After joining the University of Technology in Košice in 1952 his research was devoted to pulse circuits and digital electronics. The result of these activities, published in local and international journals, have been summarized in the book *Bipolar Transistor in Pulse Circuits*. He directed his further activities toward numerical and graphical solutions of non-linear dynamical systems. Among the most important results were the algorithms for construction and utilization of boundary surfaces in flip-flop circuits and oscillatory systems. Currently he is Professor Emeritus of electrical engineering at the Department of Electronics and Multimedial Communications, Technical University in Košice, Slovakia.

Pavol Galajda was born in 1963 in Košice, Slovak Republic. He received the Ing. (M.Sc.) degree in electrical engineering from the FE TU in Košice and CSc. (Ph.D.) degree in radioelectronics from FEI TU in Košice, in 1986 and 1995, respectively. At present he is an assistant professor at the Department of Electronics and Multimedial Communications, FEI TU in Košice. His research interest is in nonlinear circuits theory and multiple-valued logic.

Milan Guzan was born in 1969 in Snina, Slovak Republic. He received the Ing. (M.Sc.) degree in electrical engineering from the FE TU in Košice, in 1992. At present he is an assistant professor at the Department of Theoretical Electrotechnics and Electrical Measurement. His research interest is in multiple-valued logic and sensors based on multiple-valued memories.